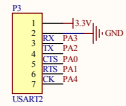


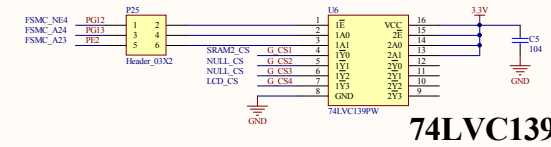
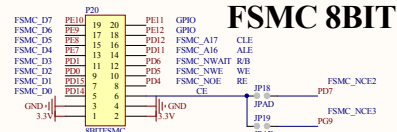
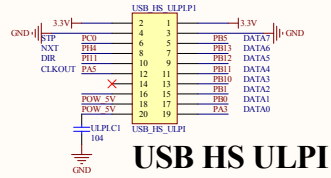
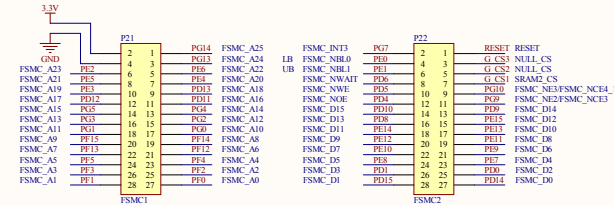
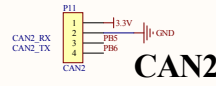
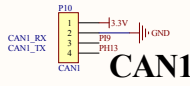
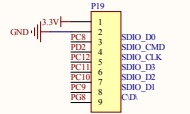
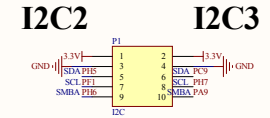
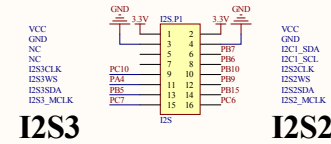
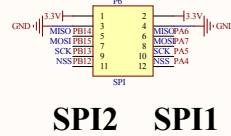
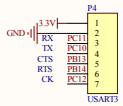
UART1



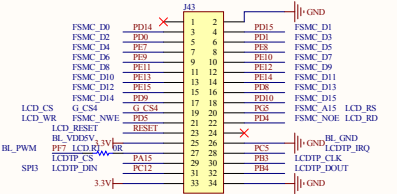
USART2



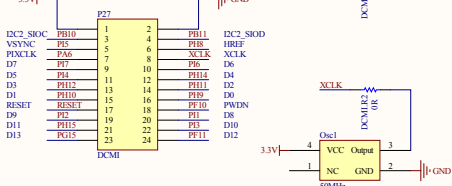
USART3



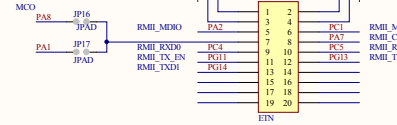
FSMC LCD



DCMI



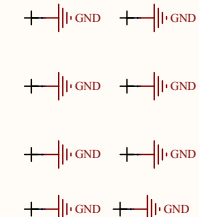
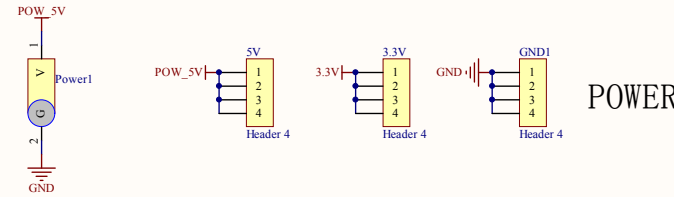
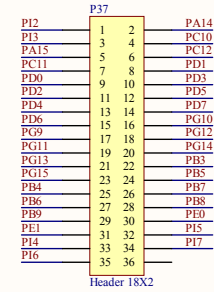
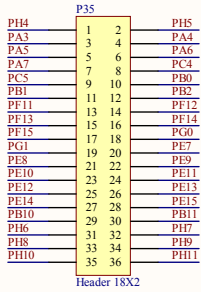
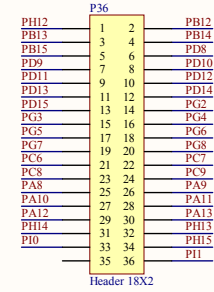
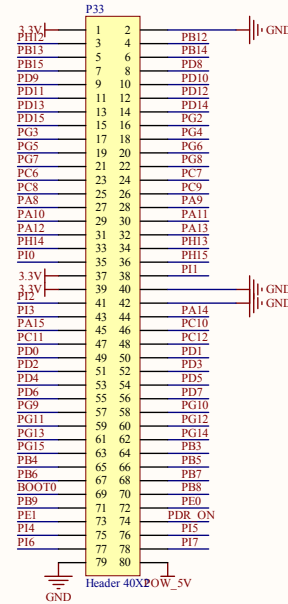
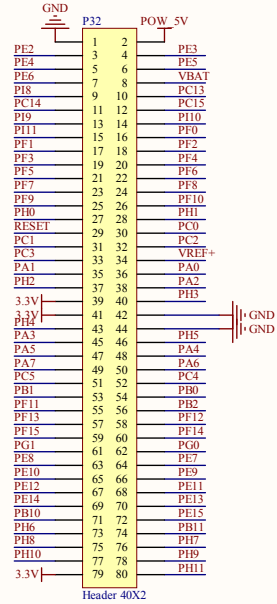
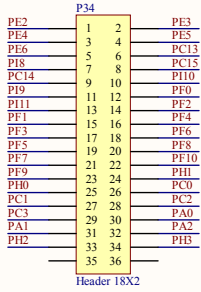
ETH



Titik		
Suc	Number	Revisan
C		
Date:	2012/8/15	Sheet of
File:	1-Pin-Interface-S-Guide	Drawing No.

ALL GPIO

Pin Port



Title		
Size	Number	Revision
A3		
Date:	2012/8/15	Sheet of
File:	E:\PCB\MIC11\SchDoc	Drawn By