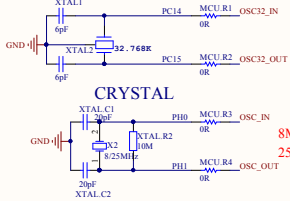
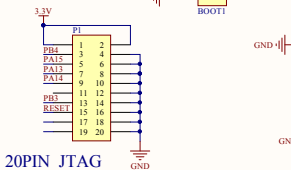
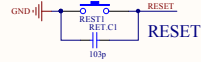
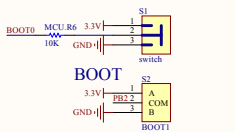
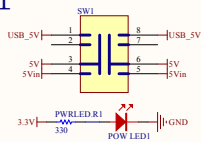
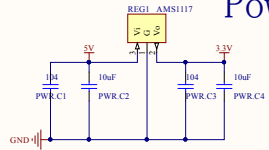


Power



PA0	34	PABW0USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA14	P00
PA1	35	PA1USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA15	P01
PA2	36	PA2USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA16	P02
PA3	37	PA3USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA17	P03
PA4	38	PA4SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA18	P04
PA5	39	PA5SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA19	P05
PA6	40	PA6SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA20	P06
PA7	41	PA7SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA21	P07
PA8	42	PA8MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA22	P08
PA9	43	PA9USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA23	P09
PA10	44	PA10USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA24	P10
PA11	45	PA11USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA25	P11
PA12	46	PA12USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA26	P12
PA13	47	PA13JTMS-SWDIO	PA27	P13
PA14	48	PA14JTK-SWCLK	PA28	P14
PA15	49	PA15JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA29	P15

PA28	P16	PA28USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA44	P30
PA29	P17	PA29USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA45	P31
PA30	P18	PA30USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA46	P32
PA31	P19	PA31USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA47	P33
PA32	P20	PA32SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA48	P34
PA33	P21	PA33SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA49	P35
PA34	P22	PA34SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA50	P36
PA35	P23	PA35SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA51	P37
PA36	P24	PA36MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA52	P38
PA37	P25	PA37USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA53	P39
PA38	P26	PA38USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA54	P40
PA39	P27	PA39USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA55	P41
PA40	P28	PA40USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA56	P42
PA41	P29	PA41JTMS-SWDIO	PA57	P43
PA42	P30	PA42JTK-SWCLK	PA58	P44
PA43	P31	PA43JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA59	P45

PA44	P46	PA44USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA70	P76
PA45	P47	PA45USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA71	P77
PA46	P48	PA46USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA72	P78
PA47	P49	PA47USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA73	P79
PA48	P50	PA48SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA74	P80
PA49	P51	PA49SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA75	P81
PA50	P52	PA50SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA76	P82
PA51	P53	PA51SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA77	P83
PA52	P54	PA52MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA78	P84
PA53	P55	PA53USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA79	P85
PA54	P56	PA54USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA80	P86
PA55	P57	PA55USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA81	P87
PA56	P58	PA56USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA82	P88
PA57	P59	PA57JTMS-SWDIO	PA83	P89
PA58	P60	PA58JTK-SWCLK	PA84	P90
PA59	P61	PA59JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA85	P91

PA60	P62	PA60USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA106	P112
PA61	P63	PA61USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA107	P113
PA62	P64	PA62USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA108	P114
PA63	P65	PA63USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA109	P115
PA64	P66	PA64SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA110	P116
PA65	P67	PA65SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA111	P117
PA66	P68	PA66SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA112	P118
PA67	P69	PA67SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA113	P119
PA68	P70	PA68MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA114	P120
PA69	P71	PA69USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA115	P121
PA70	P72	PA70USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA116	P122
PA71	P73	PA71USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA117	P123
PA72	P74	PA72USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA118	P124
PA73	P75	PA73JTMS-SWDIO	PA119	P125
PA74	P76	PA74JTK-SWCLK	PA120	P126
PA75	P77	PA75JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA121	P127

PA76	P78	PA76USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA146	P152
PA77	P79	PA77USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA147	P153
PA78	P80	PA78USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA148	P154
PA79	P81	PA79USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA149	P155
PA80	P82	PA80SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA150	P156
PA81	P83	PA81SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA151	P157
PA82	P84	PA82SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA152	P158
PA83	P85	PA83SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA153	P159
PA84	P86	PA84MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA154	P160
PA85	P87	PA85USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA155	P161
PA86	P88	PA86USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA156	P162
PA87	P89	PA87USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA157	P163
PA88	P90	PA88USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA158	P164
PA89	P91	PA89JTMS-SWDIO	PA159	P165
PA90	P92	PA90JTK-SWCLK	PA160	P166
PA91	P93	PA91JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA161	P167

PA92	P94	PA92USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA186	P192
PA93	P95	PA93USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA187	P193
PA94	P96	PA94USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA188	P194
PA95	P97	PA95USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA189	P195
PA96	P98	PA96SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA190	P196
PA97	P99	PA97SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA191	P197
PA98	P100	PA98SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA192	P198
PA99	P101	PA99SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA193	P199
PA100	P102	PA100MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA194	P200
PA101	P103	PA101USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA195	P201
PA102	P104	PA102USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA196	P202
PA103	P105	PA103USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA197	P203
PA104	P106	PA104USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA198	P204
PA105	P107	PA105JTMS-SWDIO	PA199	P205
PA106	P108	PA106JTK-SWCLK	PA200	P206
PA107	P109	PA107JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA201	P207

PA108	P110	PA108USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA226	P232
PA109	P111	PA109USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA227	P233
PA110	P112	PA110USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA228	P234
PA111	P113	PA111USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA229	P235
PA112	P114	PA112SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA230	P236
PA113	P115	PA113SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA231	P237
PA114	P116	PA114SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA232	P238
PA115	P117	PA115SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA233	P239
PA116	P118	PA116MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA234	P240
PA117	P119	PA117USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA235	P241
PA118	P120	PA118USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA236	P242
PA119	P121	PA119USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA237	P243
PA120	P122	PA120USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA238	P244
PA121	P123	PA121JTMS-SWDIO	PA239	P245
PA122	P124	PA122JTK-SWCLK	PA240	P246
PA123	P125	PA123JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA241	P247

PA124	P126	PA124USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA266	P272
PA125	P127	PA125USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA267	P273
PA126	P128	PA126USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA268	P274
PA127	P129	PA127USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA269	P275
PA128	P130	PA128SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA270	P276
PA129	P131	PA129SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA271	P277
PA130	P132	PA130SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA272	P278
PA131	P133	PA131SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA273	P279
PA132	P134	PA132MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA274	P280
PA133	P135	PA133USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA275	P281
PA134	P136	PA134USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA276	P282
PA135	P137	PA135USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA277	P283
PA136	P138	PA136USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA278	P284
PA137	P139	PA137JTMS-SWDIO	PA279	P285
PA138	P140	PA138JTK-SWCLK	PA280	P286
PA139	P141	PA139JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA281	P287

PA140	P142	PA140USART2_CTS/UART4_TX/ETH_MII_CRS/TIM3_CH1/ETR_TIMER2_ETR->ADC12_IN8/WKUP	PA306	P312
PA141	P143	PA141USART2_RTS/UART4_RX/ETH_RMII_RX_CLK/TIM3_CH2/TIM2_CH2->ADC12_IN1	PA307	P313
PA142	P144	PA142USART2_TX/TIM3_CH3/TIM9_CH1/TIM3_CH1/ETH_MDIO->ADC12_IN2	PA308	P314
PA143	P145	PA143USART2_RX/TIM3_CH4/TIM9_CH2/TIM3_CH4/OTG_HS_ULPI_D0/ETH_MII_COL->ADC12_IN3	PA309	P315
PA144	P146	PA144SPI1_NSS/SPI3_NSS/USART2_CK/DCM1_HSYN/OTG_HS_S0F/DS3_WS->ADC12_IN4/DACT1_OUT	PA310	P316
PA145	P147	PA145SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM2_CH1EN->ADC12_IN5/DACT2_OUT	PA311	P317
PA146	P148	PA146SPI1_MISO/TIM3_BKIN/TIM3_CH1/DCM1_PXCLK/TIM3_CHU/TIM1_BKIN->ADC12_IN6	PA312	P318
PA147	P149	PA147SPI1_MOSI/TIM3_CH1N/TIM3_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV->ADC12_IN7	PA313	P319
PA148	P150	PA148MCO1USART1_CK/TIM1_CH1/DC3_SCL/OTG_FS_SOF	PA314	P320
PA149	P151	PA149USART1_TX/TIM1_CH2/DC3_SMBAD/CM1_DP->OTG_FS_VBUS	PA315	P321
PA150	P152	PA150USART1_RX/TIM1_CH3/OTG_HS_ID/DCM1_D1	PA316	P322
PA151	P153	PA151USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM	PA317	P323
PA152	P154	PA152USART1_RS/CAN1_TX/TIM1_ETR/OTG_FS_DP	PA318	P324
PA153	P155	PA153JTMS-SWDIO	PA319	P325
PA154	P156	PA154JTK-SWCLK	PA320	P326
PA155	P157	PA155JTDSP3_NSS/DS3_WS/TM2_CH1_ETR/SPI1_NSS	PA321	P327

